

ltem	Performance	Test Condition			
Electrical Performance	e Test	•			
Inductance	Refer to standard electrical	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.			
DCR	characteristics list.	CH16502, Agilent33420A Micro-Ohm Mete			
Operating Temperature	-55 $^\circ\!\!\mathbb{C}$ ~+125 $^\circ\!\!\mathbb{C}$ (Including self - temperature	re rise)			
Storage temperature and Humidity range	110~+40℃,50~60%RH (Product with taj 255~+125℃(on board)	ping)			
Humidity range Saturation Current (Isat)	Approximately △L30%	Saturation DC Current (Isat) will cause L0 to drop \triangle L(%)			
Heat Rated Current (Irms)	Approximately ∆T40°C	Heat Rated Current (Irms) will cause the coil temperatur rise △T(°C). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermomete			
Reliability Test					
High Temperature Exposure(Storage) AEC-Q200		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature : 125±2°C Duration : 1000hrs Min. Measured at room temperature after placing for 24±2 hrs			
Temperature Cycling AEC-Q200		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1 : $-55\pm2^{\circ}$ C 30min Min. Step2 : $125\pm2^{\circ}$ C transition time 1min MAX. Step3 : $125\pm2^{\circ}$ C 30min Min. Step4 : Low temp. transition time 1min MAX. Number of cycles : 1000 Measured at room temperature after placing for 24±2 hrs			
Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value.	 Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) 1.Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2.Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3.Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs, keep at 25°C for 2hrs then keep at -10°C for 3hrs 4.Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs. 				
Biased Humidity (AEC-Q200)		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles) Humidity : $85\pm3\%$ R.H, Temperature : $85^{\circ}C\pm2^{\circ}C$ Duration : 1000hrs Min with 100% rated current. Measured at room temperature after placing for24±2hrs			



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Reliability Test							
High Temperature Operational Life (AEC-Q200)	 Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within±15% of initial value and shall not exceed the specification value. 	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature : 125±2°C Duration : 1000hrs Min. with 100% rated current. Measured at room temperature after placing for24±2hrs					
External Visual	Appearance:No damage	Inspect device construction, marking and workmanship. Electrical Test not required.					
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement					
Resistance to Solvents	Appearance:No damage.	Add aqueous wash chemical - OKEM clean or equivalent.					
	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Тур	e Peak value (g's)	Norm duration (Wave form	Velocity change (Vi)ft/sec
Mechanical Shock		SM	D 100	6		Half-sine	12.3
		Lead 100 6 Half-sine 12.3 shocks in each direction along 3 perpendicular axes.					
Vibration		Osci Equi Tota Test		ency: 10~ ation che .52mm±1	~2K~1 ecker 0% 0 minut	10Hz for 2	0 minute
Resistance to Soldering Heat Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the	Temperature($^{\circ}$) Time(s) Tramp/immersion		te neat cycles				
Thermal shock (AEC-Q200)	specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1 : $-55\pm2^{\circ}$ 15 \pm 1min Step2 : 125 $\pm2^{\circ}$ within 20Sec. Step3 : 125 $\pm2^{\circ}$ 15 \pm 1min Number of cycles : 300 Measured at room fempraturc after placing fo24 \pm 2hrs					



Performance	Test Condition
Appearance : No damage.	lp 90% +t, Time (ns)
More than 95% of the terminal electrode should be covered with solder 。	Steam Aging: 16 hours \pm 15 min Preheat: 150°C,60sec. Solder: Sn96.5% Ag3% Cu0. 5% Temperature: 245 \pm 5°C ° Flux for lead free: Rosin. 9.5% ° Dip time: 4 \pm 1sec. Depth: completely cover the termination
Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation
Electrical Test not required	V-0 or V-1 are acceptable.
Appearance : No damage	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. Support Solder Chip Printed circuit board before testing table 45±2 to the applied force to the board before testing Radius 340 Probe to exet bending force The direct board under test Displacement
	Appearance : No damage. More than 95% of the terminal electrode should be covered with solder • Refer Specification for Approval Electrical Test not required



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Reliability Test		
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.



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Reliability Test			
Soldering		NTEK terminations are suitable for all wave and annot be avoided, the preferred technique is the	
Lead Free Solder re- low:	Recommended temperature profiles for re-flo	w soldering in Figure 1.	
Soldering Iron(Figure 2):	limitations.	liscouraged due to the inherent process control ployed the following precautions are recommended. er of 1.0mm	
Reflow Soldering PRE-Ht () () () () () () () () () ()	COOLING TP(260°C / 10s max.)	Iron Soldering PRE-HEATING SOLDERING NATURAL COOLING 350 150 Over 60s Gradual cooling	